

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Group Art Unit:
)	
Gary A. Kinstler)	Examiner:
)	
Application No.)	
)	
Filed:)	
)	
For: METHOD AND SYSTEMS FOR A)	
RADIATION TOLERANT BUS		
INTERFACE CIRCUIT		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
WITHIN THREE MONTHS OF FILING OR
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. section 1.97(b))**

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Transmittal of Information Disclosure Statement
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The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Respectfully submitted,

Dated: March 30, 2004

By: 

David E. Crawford, Jr.
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Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	
				Filing Date	
				First Named Inventor Gary A. Kinstler	
				Group Art Unit	
				Examiner Name	
(use as many sheets as necessary)				Attorney Docket Number 60000500-1024	
Sheet	1	of	1		

OTHER ITEMS – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	TAI et al., COTS-Based Fault Tolerance in Deep Space: Qualitative and Quantitative Analyses of a Bus Network Architecture, 4th IEEE Intl. Symposium on High Assurance Systems Engineering, November, 1999, pp. 1-8.	

Examiner Signature		Date Considered	
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